APTENDIX H

```
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* Module Name:
                        mfm allocate.c
* Module Description:
#include
            "mfm_allocate.h"
            "AvidGlobals.h"
#include
#include
            "expansionDefs.h"
#include
            "LinkList.h"
            "mfm.h"
#include
#include
            "disk mac.h"
#include
            "memrtns.h"
#include
            "Digitize.h"
#include
            "LogicalToPhysical.h"
#include
            "channel.h"
#include
            "ResourceBible.h"
#include
            "env.h"
#include
            "uid.h"
#include
            "MacUtils.h"
#include
            "DebugUtils.h"
            "VolumeMenu.h"
#include
#include
            "JPEGUtils.h"
#include
            "Exception.h"
#include
            "dialogUtils.h"
#include
            "FSutils.h"
#include
            "BaseErrorDefs.h"
#include
            "autorequest.h"
#include
            "RescurceDefs.h"
            "videoDefs.h"
finclude
#define BREATHING ROOM 200
                                /* KB to leave for directory expansion */
#define DIG MODE
#define LOG MODE
typedef struct
    {
   MFM_CRUX
                CILIX;
   short
                vRef;
   channel_t
                channel;
   long
                bytesPerSec:
    long
                blocksTcAlloc;
    long
                block31ze;
   } mfm_alloc_t,
    * mfm alloc ptr,
   **mfm alloc hdl;
/***** Static Variables *****/
static listID
                        alloc = NIL:
static u_long
                        ApproxFrameSize = 11;
static char
                        TheCapMode
                                      = DIG_MCDE;
                                                                     // DIG_MODE, LOG_MODE
static Ftype_t
                        theftype
                                        = 0;
static float
                                        ≖ 0;
                        theCapRate
                                                                     // When these are zero the cruxes are clo.
static MFM_CRUX
                        theVcrux
                                        = 0;
static MFM_CRUX
                                        = 0;
                         theAlcrux
static MEM CRUX
                        cheA2crux
                                        = 0;
static short
                        cheVvref
                                        = BAD VREFNUM,
                        theAvref
                                        = BAD VREFNUM;
                        theSampsPerSec = 0,
static long
                        theBytesPerSamp = 0;
                                                                     // the minimum of the times available in
static long
                        cheTimeAvail
                                      = 0;
```

```
// more likely to catch bugs by initing t
static videoFormat_t
                          theVideoFormat = PAL_f;
static sourceFormat t theSourceFormat = VIOEO_f;
static videoType_t theVideoType = (8, VMH);
                          theVideoType = (8, VMHiResHiColor);
                                                                         // HACK for now
static capture_mask_t theCaptureMask = 0L;
static capture mask_t theResultMask = 0L;
                      theCapShift
static u_char
                                          = 0;
static u_char
                          theResultShift = 0;
static channel_t
static channel_t theChannels = 0;
static audioClock_t theAudioClock = Clock44100;
                          theChannels
static audioRate_t theAudioRate = halfRate;
static Boolean
                          theAudioMixed = FALSE;
static Boolean
                          useEmptlestVideo = TRUE,
                          useEmptiestAudio = TRUE:
/***** Defined Below *****/
static void setVinfo(Ftype_t Ftype, capture_mask_t captureMask, u_char captureShift, float capturestatic void ammIttem2Val(short mitem, audioClock_t *audioClock, audioRate_t *audioRate_t long *audioClock static MFM_CRUX mfallocCreate(long bytesPerSec, short vref, channel_t channel, Boolean preflight);
static void
                          mfaAllocCalc(Boolean preflight); .
static void
                          mfaAllocEnd(void);
static void
                          mfaAllocPunt(void);
static long
                          TotalBytes(short theVref);
static void
                          checkVolumeSettings(void);
static videoModifier_t getVideoModifier(short iQuality, short cQuality);
/**********************************
/**** Public Code
 * mfaSetSettings
Boolean mfaSetSettings( channel t chans, float capRate, u_char phase,
                           audioClock_t audioClock, audioRate_t audioRate, Boolean audioMixed,
                           short Vvref, short Avref, videoType_t video_type)
    Boolean
                 needsReinit;
    mfaForgetFiles (OUT_ALL);
    needsReinit = FALSE:
     if(!CksumValid(ck 44khz) && audioFate == fullRate)
         audioRate = halfRate:
    if (:CksumValid(ck_48khz))
         audioClock = Clock44100;
    if (theCapMode = DIG_MODE 44 (theChannels != chans );
                                       theCapRate := capRate (i
                                       theAudioClock != audioClock !!
                                       theAudioRate != audioRate !!
                                       theAudioMixed != audioMixed))
             needsReinit = TRUE;
         Set the mfm allocate statics
    theChannels
                      = chans: · '
                      = capRate;
    theCapRate
    theAudioClock = audioClock;
    theAudioRate
                      = audicRate:
    cheAudioMixed = audioMixed:
                = Avref;
                     = Vvref;
    theVvref
    theAvref
    useEmptlestVideo = (theVvref = BAD_VREFNUM);
```

```
useEmptiestAudio = (theAvref == BAD VREFNUM);
xprotect
    checkVolumeSettings ():
xexception
    if (!xcodeEquals (MFA NO MEDIA DRIVES))
        xpropagate();
    auto_request("You will not be able digitize until a valid\nmedia volume is placed online.", "OK", 1);
xend:
    Setup video capture mode info
switch( (int) (theCapRate*10))
    case 240:
        if ( phase = 0)
                 setVinfo (FULL, 0xD8000000L, 0, 24.0, 0x80000000L, 0);
                                                                                                            ,4 ou
                                                                                     // 1101 lxxx
        else if ( phase == 1)
                 setVinfo (FULL, 0xB8000000L, 0, 24.0, 0x80000000L, 0);
                                                                                     // 1011 1xxx
                                                                                                            ,4 ou
        else if ( phase == 3)
                 setVinfo (FULL, 0x78000000L, 1, 24.0, 0x80000000L, 0);
                                                                                     // 0111 lxxx
                                                                                                            , 4 ou
        else
                 setVinfo (FULL, 0xE8000000L, 0, 24.0, 0x80000000L, 0);
                                                                                     // 1110 1xxx
                                                                                                            ,4 cu
        break;
    case 120:
        if ( phase = 0 || phase = 1)
                 setVinfo (FULL, 0x48000000L, 2, 12.0, 0x40000000L, 1);
                                                                                     // 0100 lxxx
                                                                                                            ,2 ou
        else
                 setVinfo (FULL, 0x28000000L, 2, 12.0, 0x40000000L, 1);
                                                                                     // 0010 lxxx
                                                                                                            ,2 cur
        break:
                                                                                                            ,1 ou
    case 60:
                 setVinfo (FULL, 0x08000000L, 4, 6.0, 0x10000000L, 3); break; // 0000 lxxx
                                                                                                           ,1 ou
    case 300:
                 setVinfo (FULL, 0x800000001, 0, 30.0, 0x800000001, 0); break; // lxxx xxxx
                 setVinfo (FULL, 0x400000001, 1, 15.0, 0x400000001, 1); break; // 0lxx xxxx
                                                                                                            ,1 cm
    case 150:
                setVinfo (FULL, 0x20000000L, 2, 10.0, 0x20000000L, 2); break; // 001x xxxx setVinfo (FULL, 0x80000000L, 0, 25.0, 0x80000000L, 0); break; // 1xxx xxxx setVinfo (FULL, 0x40000000L, 1, 12.5, 0x40000000L, 1); break; // 01xx xxxx
                                                                                                            ,1 ou
    case 100:
    case 250:
                                                                                                            ,1 cu
                                                                                                            ,1 ou
    case 125:
               setVinfo (FULL, 0x08000000L, 4, 5.0, 0x08000000L, 4); break; // 0000 lxxx
    case 50:
                                                                                                            ,1 ou
theSourceFormat = sourceFormat;
                                                                                      // Get it from global no :
                                                                                      // Get it from global no :
the Video Format = video Format;
                                                                                      // Get it from global no:
theVideoType.vcID = gVideoType.vcID;
theVideoType.videoModifier = video_type.videoModifier;
SetDigitizeCaptureMask (theCaptureMask, theCapShift);
   Setup audio capture mode info
theSampsPerSec
                     = ((audioRate = fullRate) ? (audioClockToClockRate(audioClock)) : (audioClockToClock.
theBvtesPerSamp
                     = (audioRate == fullRate ? 2 : 1);
return needsReinit;
```

3

```
* Addresses of hardware registers:
                    OfffffeaOh ; the Display-Interrupt vector location
          .set
divect
                    OfffffeaOh ;address of DPYINT trap vector Of8600000h ;video mode register
            .set
dpytrap
            .set
m∞de
                    Of8290000h ; video status register
status
            .set
            .set
                    Oc0000060h ;gsp control registers:
vsblnk
                    Oc0000070h ;total vertical lines
vtotal
            .set
            .set
                    0c0000080h
dpyctl
                    0c0000090h ;
doystrt
            .set
                    0c00000a0h ;
dpyint
            .set
                    0c00000b0h-;
            .sec
control
                    Cc00000fCh ;
hstotll
            .set
                    0c0000110h
intenb
            .set
intpend
            .set
                    0c0000120h
                    0c0000130h
convsp
            .set
                    0c0000140h ;
convdp
            .set
                    0c0000150h ;
psize
            .set
                    0c0000160h ;
            .set
pmask
                    0c0000170h ;
pmaskext
            .set
* Constants and masks:
                    0007h
                                 ;Fields in hstctll register
msginmsk
            .set
                     0070h
msgoutmsk
           .set
msginisave .set
                     0002h
                     0007h
msgindbg
            .set
msgintmsk
            .set
                     0008h
                                 ;intin field in hstctll
                     0003h
msginf2
            .set
                    0020h
msgoutisave .set
usconrapa
                     0070h
           .set
mscoutf2
            .set
                     0030h
                    0010h
msgoutinc
            .set
                     0008h
intin
            .set
intout
            .set
                    0080h
                                 ; Mask for the CONTROL register.
ctimsk
                     601fh
            .set
                                 :Bit number of Display Interrupt bit
d1
            .set
                     10
                                 ; "Display Interrupt" bit of intenb and intpend
;Bit number of Non Interlaced bit
                     1<<41
41521.5
            .set
n1
            .sat
                     14
                     i<<ni
                                 :The non-interlaced bit
notinteri
             .set
                                 ; "Capture Enable" bit of video mode register
                     8000h
ce_bit
             .set
                                 ;Bit number of Display Interrupt bit
ai.
            .set
                     10
                                 :Offset for special JPEG hardware fifo "memory space"
special
                     2000000h
            .set
                     576
palLines
                                 ; Number of lines in a frame
            .set
                                 :Number of lines in a frame
ntsclines
                     490
            .sat
                     0f8000000h ;row table main picture starting address
rowcase
             .set
                    Offfffc00h ;address of crap page
traps
             .sec
                                 ;mac row table entries
macrows
            .set
                     palLines-8 ;video rows in row table (incl color table & PAL)
VIOWS
            .set
                     pallines/2 ; Maximum + lines in a field (pal is larger) pallines/2 ; maximum + of lines in a field
            .sec
crows
maxField
             .sec
                                 ; length in bits of physical memory rows
wormsand
             .set
                     8000h
                                 :Not Vertical Blanked -- bit position in video status reg
NV5L5IT
            .set
                     4
                                 ;pitch of MAC (16-bit pixel) lines (2 kB)
                     4000h
coltca
            .set
                                 ;Shifting a number by this multiplies by dpitch
apShift
            .sec
                     14
                                 ;Pixel size constant for "psize" register ;Number of bits between pixel "hits" in output image
            .sec
                     16
pixsize
                     64
cstride
            . set
                                 ;undefined command code
chalone
                .se.
                                 normal multi-frame playback to alternate screen buffer
                .set
                                 : (UNUSED in FullRes) Pack 256*192 image
cmdPack
                 .set
                         2
                                 ;unpack still frame to vcopy double buffer area (decompress)
cmcUnpack
                 .set
                         3
                                  :unpack and show a still frame in main screen buffer
ಆಂಗ≲ರಿದವ
                 .set
                         4
                                 ;full-screen playback on an NTSC monitor
                 .sec
cmdFull
                         5
cmcBicPack
                .sec
                                ; pack a 640x480 image
                                 ; unpack an image to 640*480
cmd31gUnpack
                .sec
                         7
                                 ; unpack and combine an image
cmdUnpackAdd
                 .set
                         8
                                 ; unpack a 16 bit frame in 32 bit mode
cmdUnback16
                 .sec
                         9
cmdPack16
                 .sec
                       10
                                 ; back a 16 bit frame in 32 bit mode
```

```
* Routine to sync to an odd field:
syncodd:
sl move
            *Rscatp, Rtemp
   bcsc
            0,Rtemp
   jrnz
            sl
   поче
            *Rstatp,Rtemp
   こをまて
            3,Rtemp
   jmz
            sl
s2 nove
            *Rstapp, Rtemp
   JZJG
            0,Rtemp
   jrz
            s2
   move
            *Rstatp, Rtemp
   bcsc
            0,Rtemp
   jrz
            $2
    recs
* Routine to sync to an even field:
synceven:
s3 move
            *Rstatp, Rtemp
   コモンゼ
            0,Rtemp
   jrz
            s3
   ≡cve
            *Rstatp, Rtemp
   こをよだ
            C,Rtemp
   irz
            s3
s4 move
            *Rstatp, Rtemo
   בפכ
            0,Rtemp
   jmz
   aove
            *Rstatp, Rtemp
   בצבל
            O,Rtemp
    jmz
   recs
```

.usect "vectors", 32

vramBase frameBuf bigBuf

YENCRY

```
MARPER: origin = Offfcoccoh,
                               length = 200000h
                              length = Octoooh
   NCMAP: origin = Offf00000h,
   langth = 15
                               langth = 000100h
SECTIONS
             {} > VEC
   vectors:
   arts:
              () > NOMAR
   .data:
             () > 4CW70
   .cext:
             () > MAPPEZ
   jstatus:
             () > JSTAT
```

.long

0,0,0,0,0,0,0,0; Reserved for debug info

```
* | The following programs are the sole property of Avid Technology, Inc., |
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* General register names:
                 A0 :Temp register
Rtemp .set
                 Al ;Constant Pixels per line
Rpixcnt .set
Roixel .set
Rpxincl .set
                 A2 ;Pointer to current input pixel
A3 ;Constant # of bits to increment Rpixel to next input pixel
                 A4 ;Alternate Constant to increment Rpixel to next input pixel
Rpx1mc2 .set
Rline .set
                 A5 ;Constant Pitch of an input line in bits (same value as Spich)
Rpixtmp .set
                  A6 :Temp register for writing to pixel locations
                 A7 ;Constant pointer to JPEG fifo status
Rjstatp .set
                 A8 ;Counter of pixels in a line
        .set
Rx
                 A9 ;Pointer to next input line
Rnext
        .set
                  AlO ;Constant pointer to video status
Rstatp .set
Rblack .set
                  All ; Pointer to a black pixel
Rtemp2 .set
                  A12
        .set
R13
                 A13
R14
        .set
                  A14
Saddr .set
                  BO ; Source pixel array starting address
                  B1 ; Source pitch (# of bits from one line to next)
Sptch .set
Offset .set
                  B4 ;Base address of source pixel array
                  B7 :Pixel array dimensions(rows:columns)
Зxv
        .set
                  B9 ;Constant: lines per frame
Rlincht .set
                  310 :Counter: lines per frame
        .set
                  B11 ;Bit mask: frame skipper
Rcapture .set
                  B12 ;Bit mask: used to reinit Rcapture
Rloadcap .set
RB13
       .set
                  B13
RB14
                  B14
        .set
                           ;Constant for "pmask" register (kill alpha chan)
                  accon
plxmsk .set
spitch .set
                  8000h
                          :Constant for "Sptch" register (4 kBytes in bits)
                  "equates.i"
         , CODY
jstatus .usect "jstatus",16
                                            ;JPEG fifo status
* Args TO <- and FROM -> the NuVista processor:
initem .usect "args",32 ;<-initial capture mask
captmsk .usect "args",32 ;<-reload capture mask overrun .usect "args",32 ;->number of overruns detected (inited by Mac) frames .usect "args",32 ;->number of frames seen (inited by Mac) tdummyl .usect "args",32 ;"fence" arg in other µcode <-
tdummy2 .usect "args",32 ;"fencerr" arg in other pcode ->
tx .usect "args",32 ;<-number of x locs to hit
        .usect "args",32 ;<-number of v locs (lines) to hit
1 .usect "args",32 ;<-stride in bits between input pixel locs
2 .usect "args",32 ;<-alt stride in bits between input pixel locs
<u>-y</u>
tstridel .usect "args",32
tstride2 .usect "args",32
idelay .usect "args",32
                              ;<-amount of delay before capturing each line (default = 1)</pre>
                  "captureMacros.1"
         .copy
stack: .bes
                  4000h ;Stack space (2kB) for calls and interrupts
    .page
    .cext
    .align
Flag:
                               ; Debug: Current value of pixel fifo status
    .long
Dat:
```

```
* Start of main program
    .def
            main
_main
    setf
            16,0,0
                            ; Field zero is 16-bit unsigned
    setf
            32,0,1
                            ; Field one is 32-bit unsigned
    movi
            stack, sp
                            ; Load stack pointer
    mov1
            spitch, Sptch
                            ; Load contstant number of bits per line
    move
            Sptch, Rline
    movi
            pixmsk, Rtemp
                            ; Init pixel mask
    move
            Rtemp, &pmask
    move
            Rtemp, &pmaskext
            jstatus+8,Rjstatp ; Load pointer to JPEG status register
    movi
    clr
                            : Clear pixel temp
    mov1
            status, Rstatp
                           ; Load pointer to video status register
   Clear DONE and wait for GO:
    clr
            Rtemp
            Rtemp, @hstctll ; clear msgout (the DONE bit and interrupt bits) to host
    movb
hosths:
            Chstctll, Rtemp ; read host control register
    movb
    andi
            msginmsk, Rtemp ; mask message
    irz
            hosths
                            ; wait for GO signal (any non-zero value)
    mov1
            msgoutinc, Rtemp
    #ove
            Rtemp, Chstctll ; set indicator to let host know we have started
   Get some host args into registers:
    move
            @tx,Rpixcat,l
                                ;number of stores in x
                                ; number of lines in frame
    move
            @ty,Rlincnt,l
    move
            Etstridel, Rpxincl, 1 ; number of bits between pixels
    move
            @tstride2,Rpxinc2,1 ;alt number of bits between pixels
   For debug, write parameters back to memory:
                                ;get addr of debug dump area
    nov1
            Dat, Rtemp
    ∴cve
            Rpixcht, *Rtemo+, 1
                               ; x
            @ty,*Rtemp+,1
    move
                                ; y
    move
            Rpxincl, *Rtemp+, 1
                                ;stride 1
            Rpxinc2, *Rtemp+, 1
    cove
                                ;stride 2
   move
            Rline, *Rtemp+,1
                                ;source pitch in bits (number of bits from one line to the next)
* N.B. The x argument (Rpixont) MUST be a multiple of 32!
    ira
            5, Rpixent
                                ;divide line length (x) by 32 for unrolled loop
   callr
            syncodd
                                :FIRST TIME: Wait for start of odd field
    move...
            Emode, Rtemp
    ori
            ce bit, Rtemp
                                ;set the global capture enable bit (begins digitizing)
    acve
            Rcemp, Emode
    move
            @initcm, Rcapture, 1 : load initial capture mask
    avoa
            Scaptmsk, Rloadcap, 1 ; load value to reinitialize capture mask
    mov1
            black-special, Rblack
                                    ;accress of black ("0")
    סנדל
            frame
black:
    .long
    .alicn
                                ;align the following code with the I-cache
skpfram:
                                ; come here to skip capturing a frame
   cailr
```

synceven

syncodd

* Attempt capturing a frame:

callr

```
frame:
* Count the frame (N.B. We must count every frame seen, whether captured or skipped):
            Oframes, Rtemp, 1
    move
            1, Rtemp
                                 ;count
    addk
            Rtemp, @frames, 1
    move
* Decide whether this is a frame we want, based on capture mask:
                                 ;check next mask bit (it goes to C-bit)
            1, Rcapture
    sll
                                 ;skip this frame if C-bit is zero (last active bit guaranteed to be 1)
    jmc
            skpfram
                                 ;check if need to reload mask bits: yes->fall thru
            mskok
    jmz
                                ;reload the capture mask (32 bits) for next time
            Rloadcap, Rcapture
    move
mskok:
* Prepare for "lines" loop:
            avramBase, Rpixel, 1
    aove
            special, Roixel, l
    subi
            capture-special, Rpixel ; starting address of video frame bufr (Special space)
    movi
                                 ; remember address of first line
            Rpixel, Rnext
    move
                                 ;get number of lines in frame
    move
            Rlincht, Ry
* Check video field (s/b ODD from compressing prev frame or from syncodd after hosths or skpfram).
* (N.B. Assumes compression takes more than one field time (~1/60th second), but less than a frame time.)
                                 ; wait for start of even field (i.e. digitizing complete)
            syncevck
* Add 8 lines of black to the top of the picture:
                                 ;eight groups of one line
             8, Rtemp2
blk
                                  ;pixels-per-line / 32
    move
             Rpixcnt, Rx
                                  ;calc the loop count ( *32 ^ /4hits-per-loop)
    sll
             5-2, Rx
loop2b:
                                  ;read JPEG pixel fifo status
             *Ristatp, Rtemp
    dvom
                                  : * * * debug * * *
    move
             Rtemp, @Flag, 0
                                  ; wait until fifo ready (bit 7 = 1)
             loop2b
    jmn
blkloop
                                 ;each write causes auto xfer(s) to JPEG pixel fifo.
             Rpixtmp, *Rblack, 0
    wone
             Rpixtmp, *Rblack, 0
    move
             Rpixtmp, *Rblack,0
    move
             Rpixtmp, *Rblack, 0
    wove
    ds†s
             Rx, blkloop
                                  ;1 line of pixels
    ds js
             Rtemp2,blk
* Send frame interrupt to the Mac:
                              get hatcall value;
             Chstctll, Rtemp
    move
                                  ;set interrupt bit
    ori
             intout, Rtemp
                                  ; send to host to indicate frame start
             Rtemp, @hstctll
    move
* Start of loop to process all lines of a frame:
lines:
                                  ;calc addr of next line
             Rline, Rnext
    add
                                  ; (re)load x count (pixels-per-line / 32)
             Rpixcnt, Rx
    ::cve
                                  :DEBUG
             @tdelay, Rtemp2, 1
    move
*loop2d:
             Rtemp2,1oop2d
                                  ;DEBUG
    dsjs
100021:
                                  ;read JPEG pixel fifo status
             ¬Rjstatp,Rtemp
    ...cvb
             Rtemp, 3Flag, 0
                                  ; * * * debug * * *
     ::.cve
                                  ; wait until fifo ready (bit7 = 1)
             10002
     jmn
locp2:
                                  ; this write causes auto xfer(s) to JPEG pixel fifo.
             Rpixtmp, *Rpixel, 0
    move
                                  now advance to next pixel
     acid
             Rpxincl, Rpixel
             Rpixtmp, *Rpixel, 0
     move
             Rpxinc2, Rpixel
     add
             Rpixtmp, *Rpixel,0
                                  ;3
     move
             Roxinci, Roixel
     add
             Rpixtmp, *Rpixel, 0
     nove
                                  ; 4
             Roxinc2, Roixel
     add
```

...ove

Rpixtmp, *Rpixel, 0

: 5

```
Rpxincl, Rpixel
<u> </u>
         Rpixtmp, *Rpixel, 0
move
add
         Rpxinc2, Rpixel
         Rpixtmp, *Rpixel, 0
                               ;7
move
add
         Rpxincl, Rpixel
         Rpixtmp, *Rpixel, 0
                               ;8
move
         Rpxinc2, Rpixel
add
         Rpixtmp, *Rpixel, 0
                               ;9
move
         Rpxincl, Rpixel
add
move
         Rpixtmp, *Rpixel,0
         Rpxinc2, Rpixel
add
         Rpixtmp, *Rpixel, 0
                               ;11
move
         Rpxincl, Rpixel
add
         Rpixtmp, *Rpixel, C
move
add
         Roxinc2, Rpixel
         Rpixtmp, *Rpixel, 0
move
         Rpxincl, Rpixel
add
         Rpixtmp, *Rpixel, 0
                               ;14
move
         Rpxinc2, Rpixel
add
         Rpixtmp, *Rpixel, 0
                                ;15
move
add
         Rpxincl, Rpixel
         Rpixtmp, *Rpixel, 0
                                ;16
move
         Rpxinc2, Rpixel
add
                                ;17
         Rpixtmp, *Rpixel, 0
move
         Rpxincl, Rpixel
add
         Rpixtmp, *Rpixel, 0
                                ;18
move
add
         Rpxinc2, Rpixel
         Rpixtmp, *Rpixel, 0
                                ;19
move
add
         Rpxincl, Rpixel
         Rpixtmp, *Rpixel, 0
                                ;20
move
         Ppxinc2, Rpixel
add
         Rpixtmp, *Rpixel, 0
                                ;21
move
         Rpxincl, Rpixel
add
         Rpixtmp, *Rpixel, 0
move
add
         Rpxinc2, Rpixel
         Rpixtmp, *Rpixel, 0
                                ;23
move
         Roxincl, Roixel
add
move
         Rpixtmp, *Rpixel, 0
                                ;24
         Rpxinc2, Rpixel
acid
         Rpixtmp, *Rpixel, 0
                                ;25
move
         Roxincl, Roixel
acid
         Rpixtmp, *Rpixel, 0
                                :26
aove
add
         Rpxinc2, Rpixel
                                ;27
         Rpixtmp, *Rpixel, 0
move
         Roxincl, Roixel
add
                                ;28
          Rpixtmp, *Rpixel, 0
move
add
          Rpxinc2, Rpixel
                                ;29
          Rpixtmp, *Rpixel, 0
move
          Rpxincl, Rpixel
a \dot{\infty} \dot{d}
          Rpixcmp, *9pixel, 0
                                ; 30
move
          Rpx1nc2, Rpixel
add
          Rpixtmp, *Rpixel, 0
                                ;31
nove
          Roxincl, Roixel
 aód
          Rpixtmo, *Spixel,0
                                ; 32
zove
 ĕòdi
          Rpxinc2, Rpixel
          Rx, loop2
                            ;loop thru the line
ds j
                            ;load addr of next line to process
          Rnext, Rpixel
 nove
                            ;loop for next line
 as j
          Ry, lines
                                 ; If we're in odd field, it took too long. .
          syncodd
 callr
          frame
 jruc
```

The following routines sync the code to the incoming video fields.
 Note: Since the status register is not synchronized with the 34010 instruction clock, we must always check that we get the same reading twice in a row.

_

```
* Wait for start of next even field: check to make sure field is already CDD at entry.
* (If we enter here in an even field, it means an OVERRUN has occurred.)
syncevck:
                 *Rstatp, Rtemp
s5
        move
        btst
                 0, Rtemp
                                 ; if even, go check a second time; fall thru if odd
                 s5el
        jrz
                 *Rstatp, Rtemp
s501
        move
                 0,Rtemp
        btst
                                 ; if even, go check a second time; fall thru if odd
        jrz
                 s5e1
                 *Rstatp, Rtemp
s6
        move
        btst
                 0,Rtemp
                                 ; loop as long as it remains odd
        jmz
        move
                 *Rstatp,Rtemp
        btst
                 0,Rtemp
                                 ; make sure we see it the same twice in a row
        imz
                 s6
                                 ; normal successful return at start of an even field
        rets
* come here if we found an even value one time:
                 *Rstatp, Rtemp ; perform second test for even
s5el
        move
        btst
                 0, Rtemp
                                 ; jump back if second check is okay (odd)
                 s501
        jmz
* else, fall thru
* At this point we have an overrun (two evens in a row), so count it
                 Coverrun, Rtemp, 1
        move
                                 ; In the even field already... increase overrun count
        addk
                 1, Rtemp
                 Rtemp, Coverrun, 1
        move
                                 ; We know it is even, so now we need to wait for odd
s7
        move
                 *Rstatp, Rtemp
                 0, Rtemp
        btst
                 s7
        jrz
                 *Rstatp, Rtemp
        move
        prsr
                 0,Rtemp
        jrz
                 s7
         jruc
                 s6
```

.end

EDL EDL

TITLE: UNTITLED01	<u>-</u> リー	
FCM: NON-DROP FRAME		
001 050 V C	04:11:23:21 04:11:37:19	01:00:00:00 01:00:13:28
M2 050 030.0	04:11:23:21	
002 050 V C	04:03:14:26 04:03:20:01	01:00:13:28 01:00:19:03
M2 050 030.0	04:03:14:26	
003 050 V C	04:11:37:19 04:11:55:29	01:00:19:03 01:00:37:13
M2 050 030.0	04:11:37:19	
004 050 V C	04:04:51:01 04:04:56:13	01:00:37:13 01:00:42:24
M2 050 030.0	04:04:51:01	•

	E: CNTITLE						
001	THEY_C	v	С	04:11:23:21	04:11:37:19	01:00:00:00	01:00:13:28
M2	THEY C		030.0		04:11:23:21		
002	THEY C	V	С	04:03:14:26	04:03:20:01	01:00:13:28	01:00:19:03
M2	THEY C		030.0		04:03:14:26		
003	THEY C	V	С	04:11:37:19	04:11:55:29	01:00:19:03	01:00:37:13
M2	THEY C		030.0		04:11:37:19		
004	THEY C	V	С	04:04:51:01	04:04:56:13	01:00:37:13	01:00:42:24
M2	THEY_C		030.0		04:04:51:01		
	_						

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Assemble list for edl file picture:

Seq	first edge	last edge	length	cum	Camera Roll
/-001	OPTICAL Number 1	FADE IN	1+08	1+08	EFFECT
\-002	end of optical 1 to	scene end	4+02	5+10	Flat #1
003	кЈ789876 -1370 +05	-1372 ÷05	2+01	7+11	Flat #1
/-004	Scene start to	start of optical 2	1+04	8+15	Flat #1
l I 005	OPTICAL Number 2	DISSOLVE	3+00	11+15	EFFECT
\-006	end of optical 2 to	scene end	7+05	19+04	Flat #1
007	кJ789876 -1236 +02	-1243 +09	7+08	26+12	Flat #1
/-008	Scene start to	start of optical 3	2+04	29+00	Flat #1
 \-009	OPTICAL Number 3	FADE OUT	1+08	30+08	EFFECT
010	LEADER -0000 +00	-0089 +15	90÷00	120+08	LEADER
/-011	OPTICAL Number 4	FADE IN	1+08	122+00	EFFECT
\-012	end of optical 4 to	scene end	1+08	123+08	Flat #1
013	KH123456 -5085 +05	-5091 +10	6+06	129+14	Flat ≆1
014	кЈ789876 -1399 +05	-1409 +08	10+04	140+02	Flat #1
015	LEADER -0000 +00	-0003 ÷14	3+15	144+01	LEADER
016	KH123456 -5132 +02	-5142 +04	10+03	154+04	Flat #1
017	КН123456 -5053 +15	-5057 ÷11	3÷13	158÷01	Flat #1
018	KH123456 -5083 +00	-5083 +13	0+14	158+15	Flat #1
019	KJ789876 -1244 +09	-1248 ÷09	4÷01	163+00	Flat #1
020	KJ789876 -1453 +07	-1464 +11	11+05	174+05	Flat ‡l
/-021	Scene start to	start of optical 5	5÷02	130+07	Flat #1
\-022	OPTICAL Number 5	FADE OUT	1÷08	181+15	EFFECT

Assemble Pull List (scene pull in assemble order) for edl file picture:

Tapename	Segment Name	first edge		last edge	leng	gth scene
NAB91COMPILATIONTA	APE Flat #1	KJ789876 -1441	+15	-1575	+03	133+05
NAB91COMPILATIONTA	APE Flat #1	KJ789876 -1368	+13	-1393	+07	24÷11
NAB91COMPILATIONTA	APE Flat #1	KH123456 -5019	+11	-5050	+04	30+10
NAB91COMPILATIONTA	APE Flat #1	кЈ789876 -1327	+03	-1368	÷12	4 <u>1</u> +10
NAB91COMPILATIONTA	APE Flat #1	кЈ789876 -1234	+00	-1300	+00	66+01
NAB91COMPILATIONTA	APE Flat #1	KH123456 -5050	+05	-5082	+15	32+11
NAB91COMPILATIONTA	APE Flat #1	кЈ789876 -1300	+01	-1327	+02	27+02
NAB91COMPILATIONTA	APE Flat #1	KH123456 -5083	+00	-5128	+01	45+02
NAB91COMPILATIONTA	APE Flat #1	кЈ789876 -1393	+08	-1441	÷14	48÷07
NAB91COMPILATIONTA	APE Flat #1	КН123456 -5128	÷02	-5172	+05	44+04

Pull list for edl file picture:

Seq	first edge	last edge	roll	Lab Roll	length	scene	take
004 KH12345	6 -5020 +11	see OPTICAL 2		Flat #1	1+04	1	2
008* KH12345	6 -5052 +06	see OPTICAL 3		Flat #1	2÷04	2	<u>:</u>
017* KH12345	6 -5053 +15	5057 +11		Flat #1	3+13	2	1
016 KH12345	6 -5083 +00	-5083 ÷13		Flat #1	0+14	3	2
013 KH12345	6 -5085 +05	-5091 +10		Flat #1	6 + 06	3	2
016 KH12345	6 -5132 +02	-5142 ÷04		Flat ‡1	10+03	3a	<u>.</u>
007 KJ78987	6 -1236 +02	-1243 +09		Flat #1	7÷08	б	-
019 KJ78987	6 -1244 +09	-1248 +09		Flat #1 -	4÷01	6	1
012 KJ78987	6 -1305 +03	see OPTICAL 4		Flat #1	1+08	7	1
006 KJ78987	6 -1332 +01	see OPTICAL 2		Flat #1	7+05	7	2

003	KJ789876 -1370 +05	-1372 +05	Flat #1	2+01	9	1
014	КJ789876 -1399 +05	-1409 +08	Flat #1	10+04	9	3
021	KJ789876 -1412 +08	see OPTICAL 5	Flat #1	6+02	9	3
002	KJ789876 -1447 +03	see OPTICAL 1	Flat #1	4+02	10	5
020	KJ789876 -1453 +07	-1464 +11	Flat #1	11+05	10	5
010	LEADER -0000 +00	-0089 +15	35mm LEADER	90+00		
015	LEADER -0000 +00	-0003 +14	35mm LEADER	3+15		
001	OPTICAL Number 1	FADE IN	EFFECT	1+08		
005	OPTICAL Number 2	DISSOLVE	EFFECT	3+00		
009*	OPTICAL Number 3	FADE OUT	EFFECT	1+08		-
011	OPTICAL Number 4	FADE IN	EFFECT	1+08		
022	OPTICAL Number 5	FADE OUT	EFFECT	1+08		

Scene Pull List for edl file picture:

Tapename	Lab	Roll		first edge		last edge	len	gth scene	
NAB91COMPILATIONTAL	 PE	Flat	#1	KH123456 -5019	+11	-5050	+04	30+10	
NAB91COMPILATIONTAL	PE	Flat	#1	кн123456 -5050	+05	-5082	+15	32+11	
NAB91COMPILATIONTAI	PE	Flat	#1	KH123456 -5083	÷00	-5128	÷01	45÷02	
NAB91COMPILATIONTAL	PE	Flat	#1	KH123456 -5128	÷02	-5172	÷05	44+04	
NAB91COMPILATIONTAL	PE	Flat	# 1	КJ789876 -1234	+00	-1300	+00	66+01	
NAB91COMPILATIONTAL	PE	Flat	#1	KJ789876 -1300	+01	-1327	+02	27÷02	
NAB91COMPILATIONTAL	PE	Flat	#1	кЈ789876 -1327	+03	-1368	+12	41+10	
NAB91COMPILATIONTAL	PE	Flat	#1	KJ789876 -1368	÷13	-1393	÷07	24+11	
NAB91COMPILATIONTAL	E	Flat	#1	KJ789876 -1393	÷08	-1441	+14	48+07	
NAB91COMPILATIONTAL	PE	Flat	‡ 1	KJ789876 -1441	+15	-1575	÷03	133+05	

Negative Dupe list for edl file picture:

		dupe negative start	scene	
Seq	last edge	dupe negative end	take	roll
800	KH123456 -5052 +06	KH123456 -5052 +06	2	Flat #1
	-5054 +09	KH123456 -5057 +11	1	
017	KH123456 -5053 +15		2	Flat #1
	-5057 +11		1	
009	OPTICAL Number 3	KH123456 -05054 +10	2	Flat #1
		KH123456 -05056 +07	1	

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DSLV center:

DSLV end:

Scene end:

Optical	effects	list	for	EDL	file	picture:	(5 effects)
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Optical effects lis	t for EDL file picture:	(5 effects)			
Num: 001 Cut: 001	Type: Fade-in	Length: 1+08 (24 frames)			
Edl: 001	OUT:	IN:			
3					
	Roll: BLACK	Roll: Flat #1			
	Scene:	Scene: 10			
	Take:	Take: 5			
Scene start:	BLACK				
FADE start:		KJ789876 -01445 +11			
FADE center:	BLACK	KJ789876 -01446 +06			
FADE end:	BLACK	KJ789876 -01447 +02			
Scene end:		KJ789876 -01451 +05			
Num: 002	Type: Dissolve	Length: 3+00 (48 frames)			
Cut: 005	1196. 213001.3				
Edl: 004	OUT:	IN:			
	Roll: Flat #1	Roll: Flat #1			
	Scene: 1	Scene: 7			
		Take: 2			
	KH123456 -05020 +11				
DSLV start:	KH123456 -05021 +15	KJ789876 -01329 +01			

KJ789876 -01330 ÷08

KJ789876 -01332 +00

КJ789876 -01339 +05

KH123456 -05023 +06

KH123456 -05024 +14

Num: 003 Type: Fade-out Length: 1+14 (30 frames)

Cut: 009

Edl: 007 OUT: IN:

Roll: Flat #1 Roll: BLACK

Scene: 2 Scene: Take: 1 Take:

Scene start: KH123456 -05052 +06

FADE start: KH123456 -05054 +10 BLACK KH123456 -05055 +08 FADE center: BLACK FADE end: KH123456 -05056 ÷07 BLACK Scene end: BLACK

Num: 004 Type: Fade-in Length: 1+08 (24 frames)

Cut: 011

Edl: 008 OUT: IN:

Roll: BLACK Roll: Flat #1

Scene: 7 Scene: Take: Take: 1

Scene start: BLACK

FADE start: BLACK KJ789876 -01303 +11 BLACK FADE center: KJ789876 -01304 +06 FADE end: BLACK KJ789876 -01305 +02 Scene end: KJ789876 -01306 +10

Num: 005 Type: Fade-out Length: 1+14 (30 frames)

Cut: 022

Edl: 017 OUT: IN:

Roll: Flat #1 Roll: BLACK

Scene: 9 -Scene: Take: 3 Take:

KJ799976 -01412 +08

Scene start: FADE start: FADE center: KJ789676 -01413 ÷10 BLACK KJ789876 -01419 +08 BLACK KJ789876 -01420 +07 BLACK FADE end: Scene end: BLACK